Docket No.: 67162-014 **PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shinichi MIZOGUCHI, et al.

Serial No.: : Group Art Unit:

Filed: June 24, 2003 : Examiner:

For: METHOD OF ERASING DATA OF NONVOLATILE SEMICONDUCTOR MEMORY

**UNIT** 

## INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

## . Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Facsimile: (202) 756-8087 **Date: June 24, 2003** 

| INFORMATION DISCLOSURE<br>CITATION IN AN<br>APPLICATION |  |   |                           |                    | ATTY. DOCKET NO. 67162-014   | SEF                               | SERIAL NO.                                    |     |             |  |
|---|--|---|---------------------------|--------------------|--|-----------------------------------|---|-----|-------------|--|
|   |  |   |                           |                    | APPLICANT<br>Shinichi MIZOGUCHI, et al.                                      |                                   |   |     |             |  |
| (PTO-1449)  |  |   |                           |                    | FILING DATE June 24, 2003  | GROUP                             |   |     |             |  |
|   |  |   |                           | U.S. PATEN         | IT DOCUMENTS   |                                   |   |     |             |  |
| EXAMINER'S  |  | 1   | Document Number           | Publication Date   | Name of Patentee or Appli  | cant of Cited                     | t of Cited Pages, Columns, Lines, Where       |     |             |  |
| INITIALS  | CITE<br>NO.                                      | Num   | ber-Kind Code2 (if known) | MM-DD-YYYY         | Document   |                                   | Relevant Passages or Releva<br>Figures Appear |     | Relevant    |  |
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| EXAMINER'S Foreign Patent Document Publication Date     |  |   |                           |                    |  | Pages, Colun                      | Pages, Columns, Lines                         |     | Translation |  |
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|   |  |   |                           |                    |  |                                   |   | Yes | No          |  |
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|   |  | EX  | AMINER                    |                    |  | DATE CONSIDERED                   |   |     |             |  |

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.